

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: N. TAKAHASHI et al
Serial No. 10/453,606
Filed: June 4, 2003

Group Art Unit: 2813
Examiner: C. Chaudhari
For: A METHOD OF MANUFACTURING A
SEMICONDUCTOR DEVICE AND A
SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

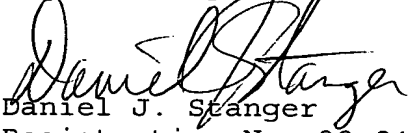
February 24, 2004

Sir:

In accordance with the duty of disclosure, Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/453,606.

Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form to indicate that the references have been considered.

Respectfully submitted,


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Date: February 24, 2004

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| FORM PTO-1449 (REV. 7-80) | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | ATTY. DOCKET NO. H-1018-03 | | SERIAL NO. | |
| LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary) | | | | APPLICANT N. TAKAHASHI et al | | | |
| | | | | FILING DATE 02/24/04 | | GROUP 2813 | |
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| | AP | | | | | | <input type="checkbox"/> <input type="checkbox"/> |
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